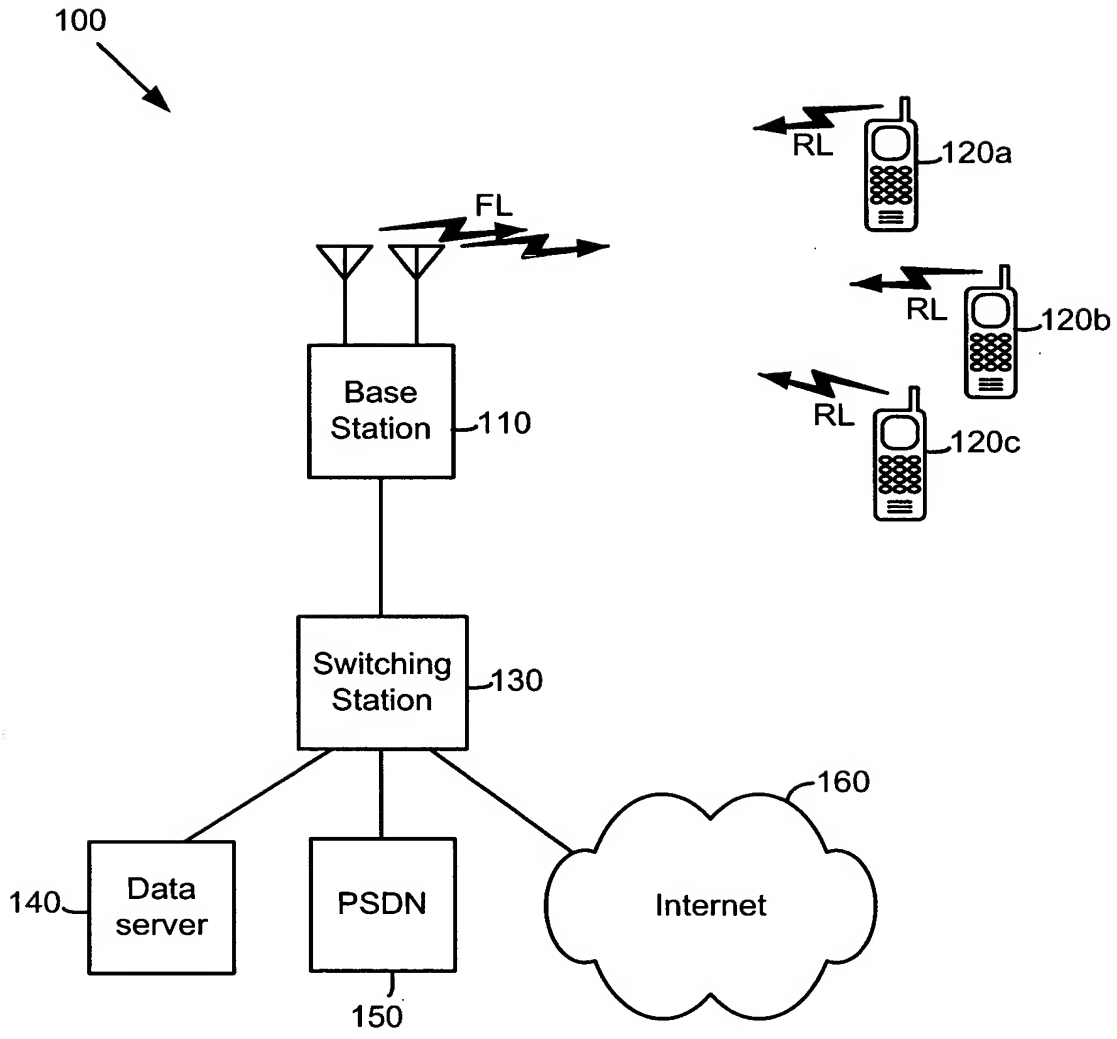
100
RL
120a
FL
RL
120b
Base Station
110
RL
120c
Switching Station
130
160
140
Data server
PSDN
Internet
150

FIG. 1

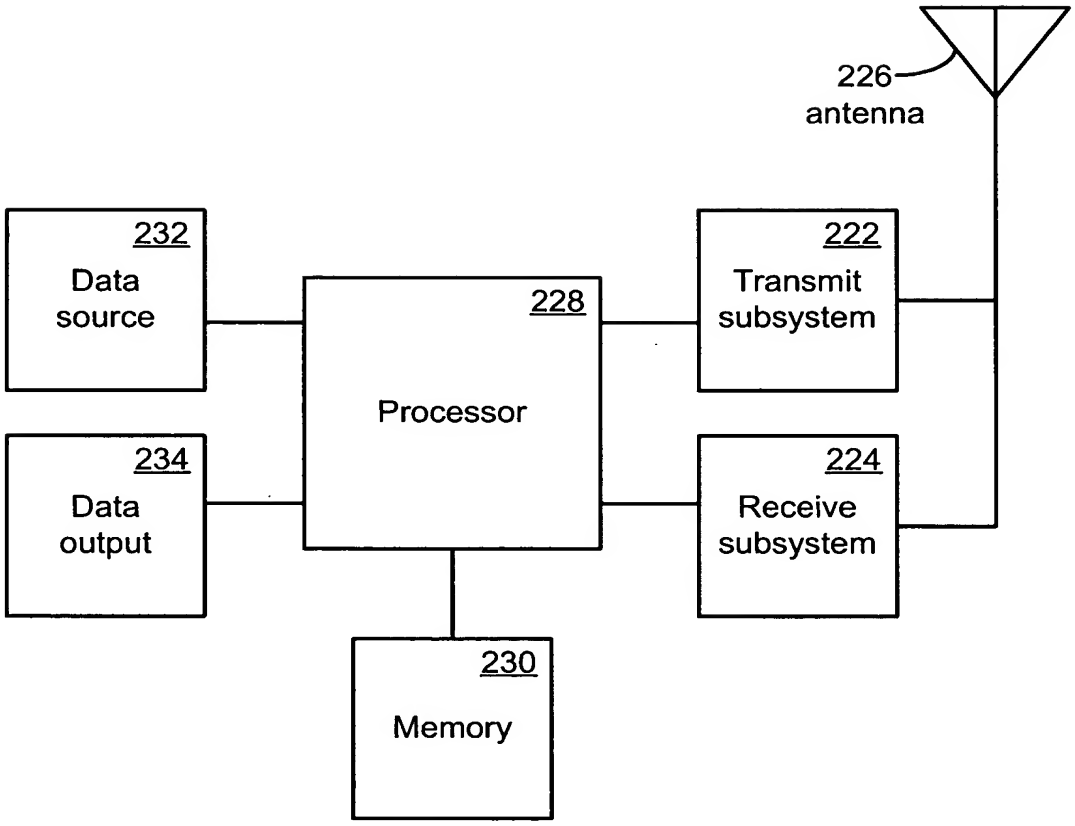
226
antenna
232
Data source
228
Processor
222
Transmit subsystem
234
Data output
224
Receive subsystem
230
Memory

FIG. 2

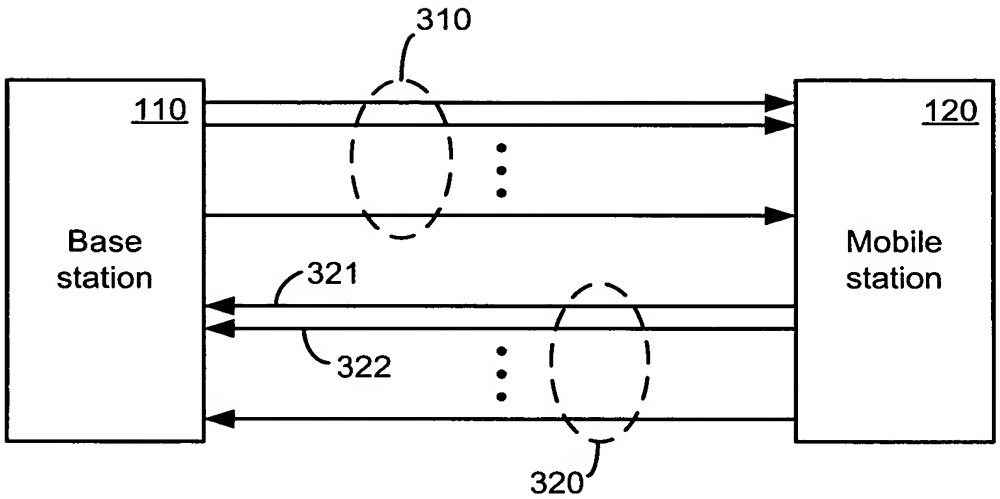

FIG. 3

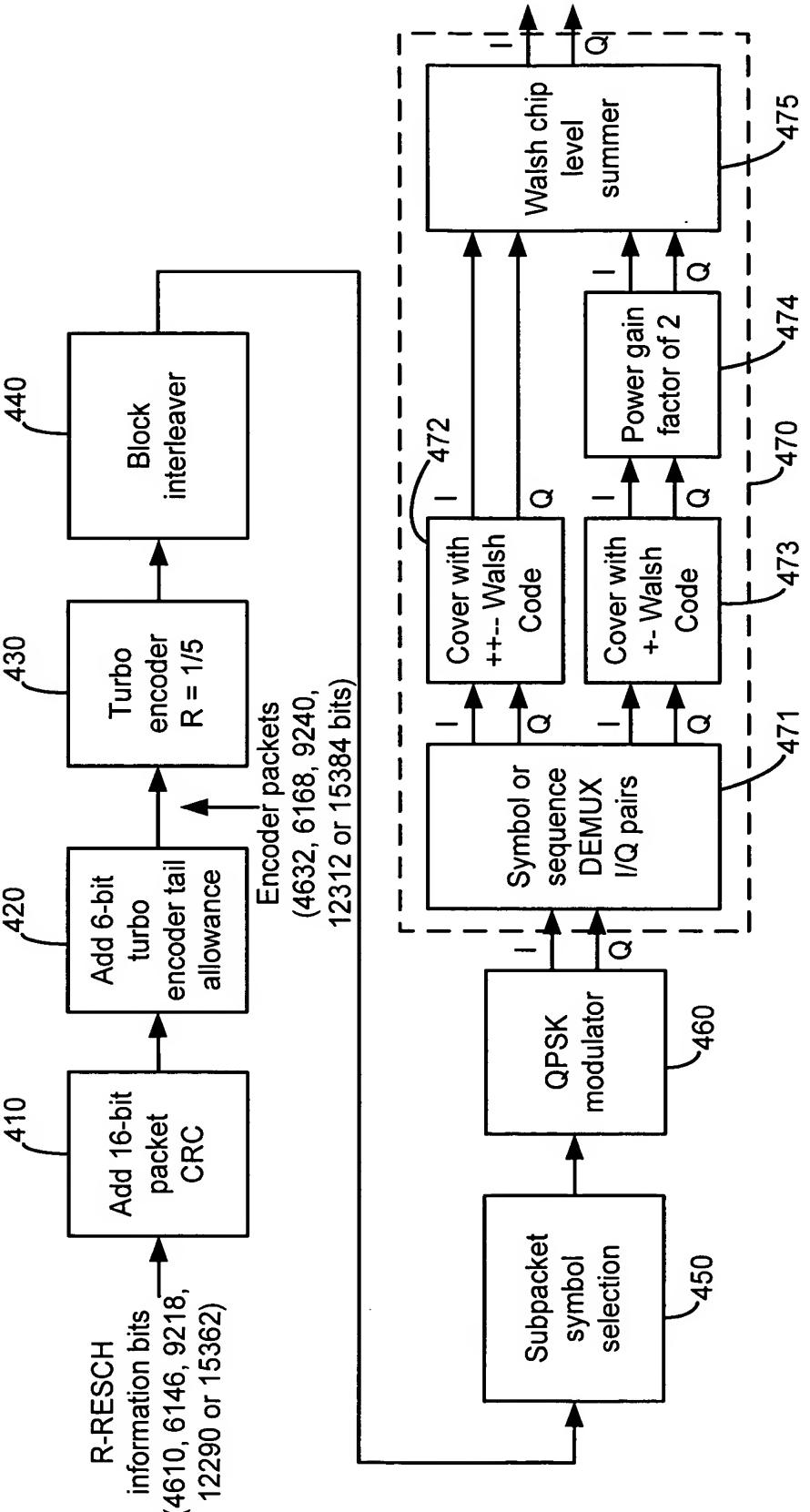
R-RESCH information bits (4610, 6146, 9218, 12290 or 15362)
410
Add 16-bit packet CRC
420
Add 6-bit turbo encoder tail allowance
430
Turbo encoder R = 1/5
440
Block interleaver
Encoder packets (4632, 6168, 9240, 12312 or 15384 bits)
450
Subpacket symbol selection
460
QPSK modulator
I
Q
471
Symbol or sequence DEMUX I/Q pairs
472
Cover with ++-- Walsh Code
473
Cover with +- Walsh Code
474
Power gain factor of 2
475
Walsh chip level summer
470

FIG. 4

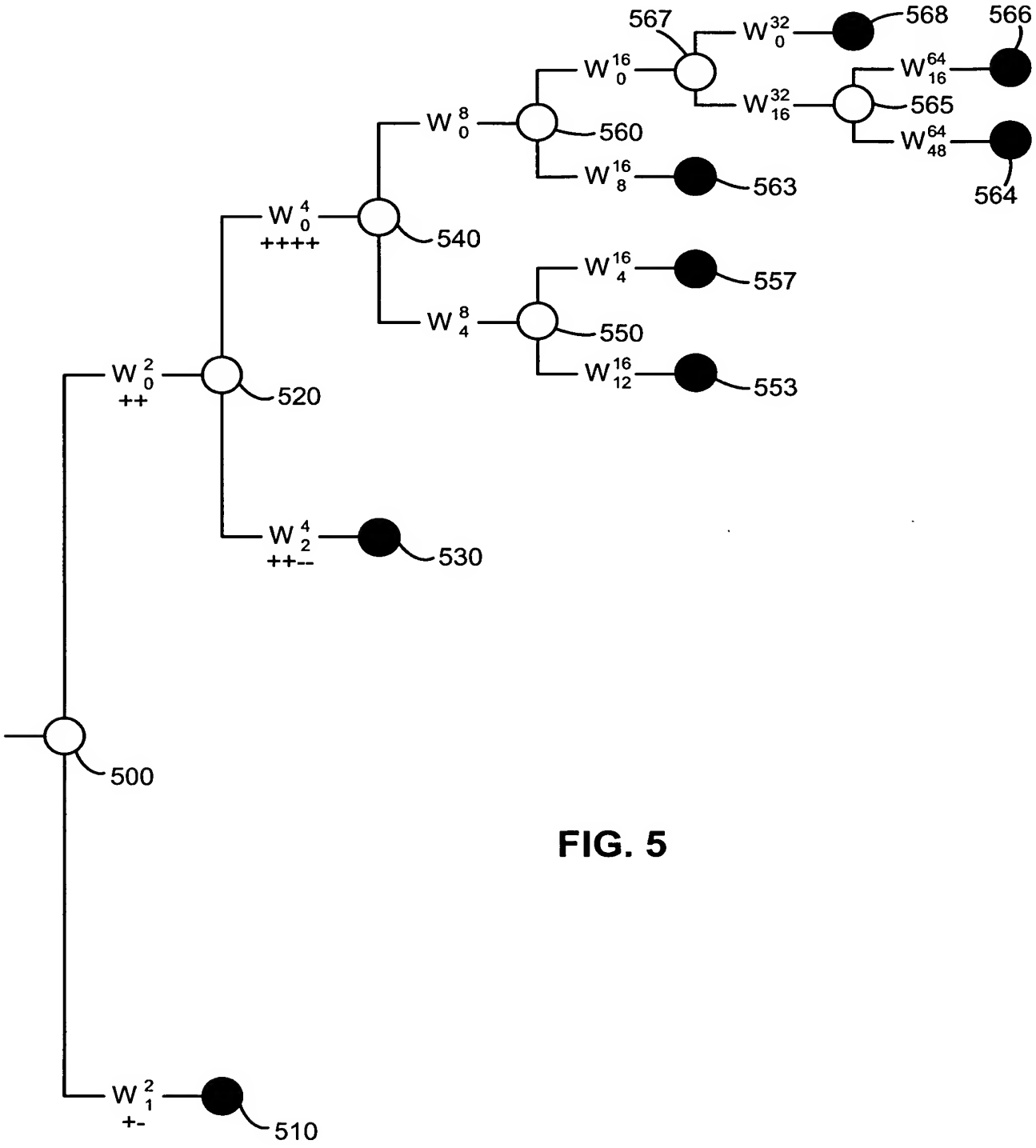
500
W_0^2 ++
520
W_1^2 +-
510
W_2^4 ++--
530
W_0^4 ++++
540
W_0^8
560
W_4^8
550
W_0^{16}
567
W_8^{16}
563
W_4^{16}
557
W_{12}^{16}
553
W_0^{32}
568
W_{16}^{32}
565
W_{16}^{64}
566
W_{48}^{64}
564

FIG. 5